

ADMINISTRATIVE-INTERNAL USE ONLY

DATA CENTER OPERATIONS BRANCH

NDS OPERATIONS PROCEDURE MANUAL
NO. P-C009

SYSTEMS SW & HW
13 April 1983

CONFIGURATION 1100/84

SYMBOLIC TITLE: N/A

ORIGINATOR:

STAT

ADMINISTRATIVE-INTERNAL USE ONLY

P-C 009

ADMINISTRATIVE-INTERNAL USE ONLY

30 September 1981

MEMORANDUM FOR: Chief, SPS

FROM

STAT

SUBJECT : 1100/84 Configuration Update

The attached 2 Diagrams are updates to the 1100/84 configuration document dated 2 September 1981. This update was necessary in order for the diagrams to correspond with the physical switch positions as connected by the UNIVAC customer engineers. In addition, the switches have been numbered on the diagrams. These numbers are being referred to in operating instructions.

STAT

Distribution:

Original - Addressee

1 - CH/PB
1 - CH/OSS
1 -
1 -
1 -
1 - UNIVAC S.A.
1 - UNIVAC C.E.
1 - COTR
1 - Originator

STAT.

ADMINISTRATIVE-INTERNAL USE ONLY

TAPE UNIT FOR SIP IS NOT AVAILABLE - CONTINUE YN
TAPE UNIT FOR I/O IS NOT AVAILABLE - CONTINUE YN

(Table 3-17)

This message indicates that SIP is configured to tape (SBFILE = 1) and a tape drive is not available.

TAPE UNIT NOT AVAILABLE - ANS GO

(Figure 12-4)

Displayed when there is not an available tape unit.

An answer of N will cause backout from SIP initialization.

Removal of the reason for failure, followed by answer Y to the message, should produce a successful SIP/Trace initialization.

site-id TERMINATING

(Table 3-4)

Displayed in response to an UR *site-id* keyin. Indicates that the specified *site-id* is terminating.

sname TERMINATING

(Table 3-4)

Displayed in response to an UR *sname* keyin. Indicates that the specified remote device is terminating.

THE DEVICE REQUESTED CANNOT BE FOUND IN THE MCT

(11.10)

This message indicates a probable configuration problem during a boot attempt.

THE DISK PACK DOES NOT HAVE A VOL1 RECORD

(11.3)

Indicates the disk pack does not have a vol1 record.

THE EQUIPMENT SPECIFIED IS UNDEFINED IN AACONFIG

(11.10)

This message indicates a probable configuration problem during a boot attempt.

THERE ARE *nnn* ADDITIONAL INCORRECT SAU POSITIONS

(4.8)

Displayed if the allowed number status messages have gone to the operator display console.

THERE IS NO CHAIN FOR EQUIPMENT CODE

(11.10)

This message indicates a probable configuration problem during a boot attempt.

THE SYSTEM DISK PACK IS NOT A DRS PACK

(11.3)

DO YOU WANT TO SELECT A NEW DEVICE? RESPOND Y OR N

Before the EXEC is copied to the disk, the label of the pack is read to verify that it has been properly prepped. If the pack is not a DRS pack, the above message is displayed.

TIMEOUT AEQT

(Table 7-4)

Processor has not received response from last function for six seconds.

TIMEOUT AET

(Table 7-2)

Processor has not received response from last function for six seconds.

site-id TIMEOUT device AT

(Table 4-4)

No response has been received from the remote 1004/DCT-2000 in an allotted time interval.
Do not answer T unless authorized by the user or the site.**sname TIMEOUT AETQ**

(Table 7-6)

0770 printer error. If repetitive, reply Q or T as desired and notify appropriate maintenance personnel.

**TIMEOUT DURING REWIND OF { DUMP } TAPE.
 { BOOT }**

(11.10)

**REWIND MANUALLY { TO LOADPOINT }
 { AND UNLOAD }**

Rewind of boot or dump tape unit has timed out.

sname TIMEOUT ERROR AET

(Tables 7-1, 9-2, and 10-1)

This high-speed card reader, 9000 card reader, and 1004 card reader error message indicates the device did not respond to a command.

sname TIMEOUT ERROR AETQ

(Tables 7-3, 9-3, and 10-1)

This high-speed punch, 9000 card punch or printer, and 1004 punch or printer error message indicates a timeout has occurred because the processor did not respond to the last command from the device.

name TIMEOUT ERROR c. ecod RECOVER? YN**

(15.5.4)

The C/SP control program displays this message if the response to a command is not received in a specific amount of time.

aaa bbb/ccc/ddd/eee TIMEOUT ggg ABGM

(6.7)

A timeout occurred.

aaa bbb/ccc/ddd/eee TIMEOUT RESOLVE AND ANSWER GO

(6.7)

This message is displayed if the I/O request has not completed in the allotted time. The format of the message is dependent upon machine type.

block-id filename TLBL\$ ERROR= octal | TLBL\$ I/O ERROR= octal |

(Table 3-7)

The I/O status encountered by TLBL\$ is printed only if the TLBL\$ error is 011.

device caunam/ioaunm/cunama TMCHK run-id AGM
device cpunam/cuname TMCHK run-id AGM

(Table 6-5)

This is an informative message indicating a timing problem with an MSA-controlled device.

CM σ
BLOCK

CM1
WORD

CM2
WORD

CM3
WORD

CM4 WORD

CM5 WORD

CM 6
WORD

CM7
BYTE

I		O		U		I	
1	TC _A	TC _A	TC _A				
2	TC _B	TC _B	TC _B				
3	TCBL (A)	TCB1 (B)	TCB1 (B)	TCB1 (A)	TCB1 (A)	TCB1 (A)	TCB1 (B)
4							
5							
6							
7							
8	DCA \emptyset (A)	DCB \emptyset (A)	DCA \emptyset (A)	DCB \emptyset (B)	DCA \emptyset (B)	DCB \emptyset (A)	DCB \emptyset (A)
A	DCB1 (B)	DCA1 (B)	DCB1 (B)	DCA1 (B)	DCB1 (B)	DCA1 (B)	DCB1 (B)
C	DCH \emptyset (A)	DCE1 (B)	DCH \emptyset (A)	DCE1 (B)	DCH \emptyset (B)	DCH \emptyset (B)	DCH \emptyset (B)
E	CSP1	CSP1	CSP1	CSP1	CSP1	CSP1	CSP1
7							
8	DCA1 (A)	DCB1 (A)	DCA1 (A)	DCB1 (A)	DCA1 (A)	DCB1 (A)	DCB1 (A)
A	DCB \emptyset (B)	DCA \emptyset (B)	DCB \emptyset (B)	DCA \emptyset (B)	DCB \emptyset (B)	DCA \emptyset (B)	DCA \emptyset (B)
C	DCH \emptyset (A)	DCF1 (B)	DCH \emptyset (A)	DCF1 (B)	DCH \emptyset (A)	DCH1 (B)	DCH1 (B)
E	CSP2	CSP2	CSP2	CSP2	CSP2	CSP2	CSP2
7							
8	DCC \emptyset (A)	DCGL (B)	DCC \emptyset (A)	DCGL (B)	DCC \emptyset (A)	DCEP (B)	DCEP (B)
A	DCD \emptyset (A)	DCDL (A)	DCD \emptyset (A)	DCDL (A)	DCD \emptyset (A)	DCDL (A)	DCDL (A)
C							
E	CSP3	CSP3	CSP3	CSP3	CSP3	DCF \emptyset (B)	DCF \emptyset (B)
8							
A	DCC1 (B)	DCH1 (B)	DCC1 (B)	DCH1 (B)	DCC1 (B)	DCF1 (B)	DCF1 (B)
C	DCD1 (B)	DCD \emptyset (B)	DCD1 (B)	DCD \emptyset (B)	DCD1 (B)	DCD \emptyset (B)	DCD \emptyset (B)
E	HSTCUI				HSTCUI	DCE1 (B)	DCE1 (B)
3							
A	DCE \emptyset (A)	DCC \emptyset (B)	DCE \emptyset (A)	DCC \emptyset (B)	DCE \emptyset (A)		
C	SSDCU \emptyset (A)	SSDCU1 (A)	SSDCU \emptyset (A)	SSDCU1 (A)	SSDCU \emptyset (A)		
E							
3	HSTCUO				HSTCUO		
A	DCF \emptyset (A)	DCC1 (A)	DCF \emptyset (A)	DCC1 (A)	DCH1 (A)	DCC \emptyset (B)	DCC \emptyset (B)
C	SSDCUL (B)	SSDCU \emptyset (B)	SSDCUL (B)	SSDCU \emptyset (B)	SSDCUL (B)	SSDCU1 (A)	SSDCU1 (A)
E							
3	HSTCUO				HSTCUO		
A	DCF \emptyset (A)	DCC1 (A)	DCF \emptyset (A)	DCC1 (A)	DCH1 (A)	DCC \emptyset (B)	DCC \emptyset (B)
C	SSDCUL (B)	SSDCU \emptyset (B)	SSDCUL (B)	SSDCU \emptyset (B)	SSDCUL (B)	SSDCU1 (A)	SSDCU1 (A)
E							
3	SAU	SAU	SAU	SAU	SAU	DCC \emptyset	DCC \emptyset
A	PRINT1	PR1	PRINT1	PR1	CON \emptyset		
C	CON \emptyset	CON \emptyset	CON \emptyset	CON \emptyset	CR1		
R	CRL _{Read1}	CR1	CRL _{Read1}	CR1	CON1		
1	CON1	CON1	CON1	CON1	PR2		
2	PR2	PR2	PR2	PR2	CON2		
3	CON2	CON2	CON2	CON2	PR2		
P	PUL _{Sync1}	PUL	PUL _{Sync1}	PUL	CON1		
U	CON3	CON3	CON3	CON3	PUL		

1100/84 TRANSFER SWITCH ASSIGNMENTS: IN 15 KIM LUN FL 04 KM 15
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DCAS (A)	DCB \emptyset (B)	DCC \emptyset (A)	DCD \emptyset (A)	SSDCU \emptyset (A)	DCAL (A)	DCBL (B)	DCCL (B)	DCD1 (B)	SSDCU1 (B)	CSP1 (A)	CSP2 (A)	CSP3 (A)	CSP1 (C)	CSP (C)	
I \emptyset I \emptyset	I \emptyset CM2 (A)	I \emptyset CM3 (A)	I \emptyset CM3 (A)	I \emptyset CM5 (A)	4 \emptyset I \emptyset	I \emptyset CM1 (A)	I \emptyset CM4 (A)	I \emptyset CM4 (A)	I \emptyset CM6 (A)	4 \emptyset I \emptyset					
I \emptyset CM1 (A)	I \emptyset CM2 (A)	I \emptyset CM3 (A)	I \emptyset CM3 (A)	I \emptyset CM5 (A)	I \emptyset CM2 (A)	I \emptyset CM1 (A)	I \emptyset CM4 (A)	I \emptyset CM4 (A)	I \emptyset CM6 (A)	I \emptyset CM1 (E)	I \emptyset CM2 (E)	I \emptyset CM3 (E)	I \emptyset CM4 (E)	I \emptyset CM5 (E)	
I \emptyset	2	3	(4)	5	6	7	8	9	10	11	12	13	14	15	
1															
DCAS (B)	DCB \emptyset (A)	DCC \emptyset (B)	DCD \emptyset (C)	SSDCU \emptyset (B)	DCAL (B)	DCBL (A)	DCCL (A)	DCD1 (A)	SSDCU1 (A)	CSP1 (B)	CSP2 (B)	CSP3 (B)	CSP2 (C)	CS1 (D)	
I \emptyset CM2 (A)	I \emptyset CM1 (A)	I \emptyset CM1 (A)	I \emptyset CM4 (A)	I \emptyset CM6 (A)	I \emptyset CM1 (A)	I \emptyset CM2 (A)	I \emptyset CM1 (A)	I \emptyset CM3 (A)	I \emptyset CM5 (A)	I \emptyset CM1 (E)	I \emptyset CM2 (E)	I \emptyset CM3 (E)	4 \emptyset I \emptyset	CSF	
4 \emptyset I \emptyset	I \emptyset CM1 (A)	I \emptyset CM5 (A)	I \emptyset CM4 (A)	I \emptyset CM6 (A)	4 \emptyset I \emptyset	I \emptyset CM2 (A)	I \emptyset CM6 (A)	I \emptyset CM3 (A)	I \emptyset CM2 (A)	4 \emptyset I \emptyset	4 \emptyset I \emptyset	4 \emptyset I \emptyset	8 \emptyset I \emptyset	CSF	
I \emptyset	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30

NON-REMOTED
 See Final
 See Final

HSTS	DCF \emptyset (A)	DCG \emptyset (A)	DCH \emptyset (A)	HSTS	HSTS	DCFL (B)	DCGL (B)	DCHL (B)	CSP (E)
I \emptyset CM4 (A) CM5 (A)	I \emptyset CM6 (A)	I \emptyset CM1 (C)	I \emptyset CM2 (C)	I \emptyset	I \emptyset CM4 (A) CM5 (A)	4 \emptyset I \emptyset	4 \emptyset I \emptyset	4 \emptyset I \emptyset	CSP1
I \emptyset T \emptyset	4 \emptyset T \emptyset	4 \emptyset T \emptyset	4 \emptyset T \emptyset	I \emptyset	I \emptyset T \emptyset	I \emptyset	I \emptyset CM6 (C)	I \emptyset CM2 (C)	CSP2

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DCAS	DCB	DCC	DCD	SSDCU
(A)	(B)	(A)	(A)	(A)
I2	I2	I2	I2	I2
CM1(?)	CM2(A)	CM3(?)	CM3(A)	CM5(A)
I2	I2	I2	I2	I2
CM1(?)	CM2(A)	CM3(?)	CM3(A)	CM5(A)

DCAL	DCBL	DCC1	DCD1	SSDCU1
(A)	(B)	(B)	(B)	(B)
I2	I2	I2	I2	I2
CM2(?)	CM1(A)	CM4(?)	CM4(A)	CM6(A)
I2	I2	I2	I2	I2
CM2(?)	CM1(A)	CM4(?)	CM4(A)	CM6(A)

CSP1	CSP2	CSP3	CSP1	CSP
(A)	(A)	(A)	(C)	(C)
I2	I2	I2	I2-I3	I2-I3
CM1(E)	CM2(E)	CM3(E)	I2	I2
I2	I2	I2	I2-I3	I2-I3
CM1(E)	CM2(E)	CM3(E)	I2	I2

DCAS	DCB	DCC	DCD	SSDCU
(B)	(A)	(B)	(B)	(B)
I3	I3	I3	I3	I3
CM2(A)	CM1(?)	CM5(?)	CM4(A)	CM6(A)
I2	I2	I2	I2	I2
CM2(A)	CM1(?)	CM5(?)	CM4(A)	CM6(A)

DCAL	DCBL	DCC1	DCD1	SSDCU1
(B)	(A)	(A)	(A)	(A)
I3	I3	I3	I3	I3
CM1(A)	CM2(?)	CM6(?)	CM3(A)	CM5(A)
I2	I2	I2	I2	I2
CM1(?)	CM2(?)	CM6(?)	CM3(A)	CM5(A)

CSP1	CSP2	CSP3	CSP2	CSP
(B)	(B)	(B)	(C)	(D)
I3	I3	I3	I2-I3	CSP
CM1(E)	CM2(E)	CM3(E)	I2	I2
I2	I2	I2	I2-I3	CSP
CM1(E)	CM2(E)	CM3(E)	I2	I2

NON- REMOTED

DCES	DCF	DCG	DCH	HSTS		DCEL	DCFL	DCGL	DCHL	CSP
(A)	(A)	(A)	(A)			(B)	(B)	(B)	(B)	(E)
I2	I2	I2	I2	GREEN		I2	I2	I2	I2	CSP1
CM5(?)	CM6(?)	CM1(c)	CM2(c)	CM4(E) I CM5(E) O		CM1(c)	CM2(c)	CM3(?)	CM4(?)	
I2	I2	I2	I2	CM1(E) I CM5(E) O		I2	I2	I2	I2	CSP1
CM1(c)	CM3(c)	CM5(?)	CM5(c)			CM1(c)	CM4(?)	CM6(c)	CM2(c)	CSP2 or CSP3

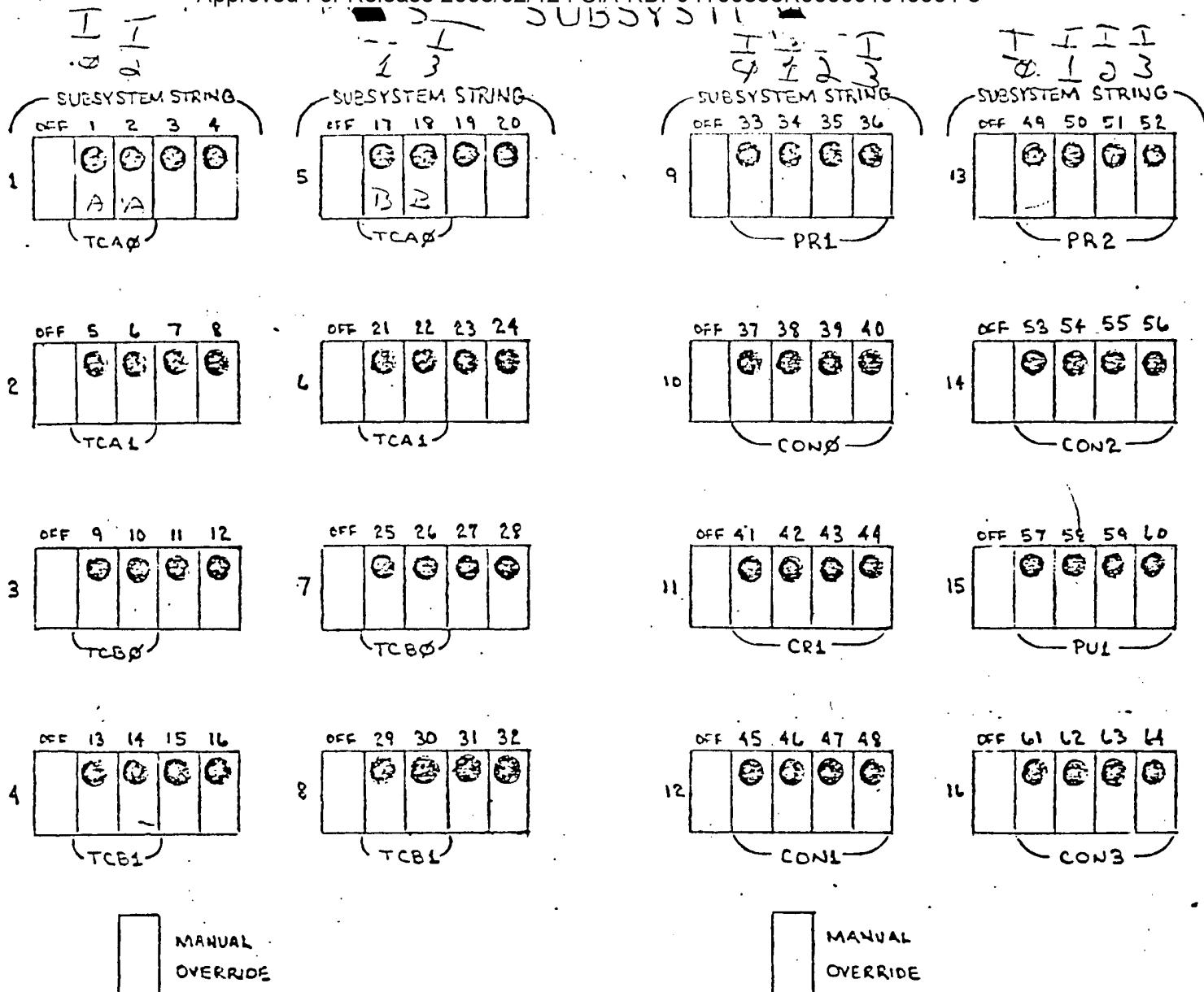
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OFF LINE	1 OFF LINE	2 OFF LINE	3 OFF LINE	4 OFF LINE	5 OFF LINE	6 OFF LINE	7 OFF LINE	8 OFF LINE	9 OFF LINE	10 OFF LINE	11 OFF LINE	12 OFF LINE
APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0
APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1
	DCA0	DCA1	DCB0	DCB1	DCC0	DCC1	DCD0	DCD1				
OFF LINE	13 OFF LINE	14 OFF LINE	15 OFF LINE	16 OFF LINE	17 OFF LINE	18 OFF LINE	19 OFF LINE	20 OFF LINE	21 OFF LINE	22 OFF LINE	23 OFF LINE	24 OFF LINE
APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0
APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1
	SSDCUP	SSDCUL										
OFF LINE	25 OFF LINE	26 OFF LINE	27 OFF LINE	28 OFF LINE	29 OFF LINE	30 OFF LINE	31 OFF LINE	32 OFF LINE	33 OFF LINE	34 OFF LINE	35 OFF LINE	36 OFF LINE
APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0
APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1
	CSP1	CSP2	CSP3				HSTCU1	HSTCU0				
OFF LINE	37 OFF LINE	38 OFF LINE	39 OFF LINE	40 OFF LINE	41 OFF LINE	42 OFF LINE	43 OFF LINE	44 OFF LINE	45 OFF LINE	46 OFF LINE	47 OFF LINE	48 OFF LINE
APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0	APPL 0
APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1	APPL 1

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SUBSYSTEMS



SAU

OFF LINE
ON LINE
SAU CLEAR

TEMP CHECK
AIR CHECK

IOU

INTERFACE EN
IOU 0

0
OFF LINE
APPL 0
APPL 1

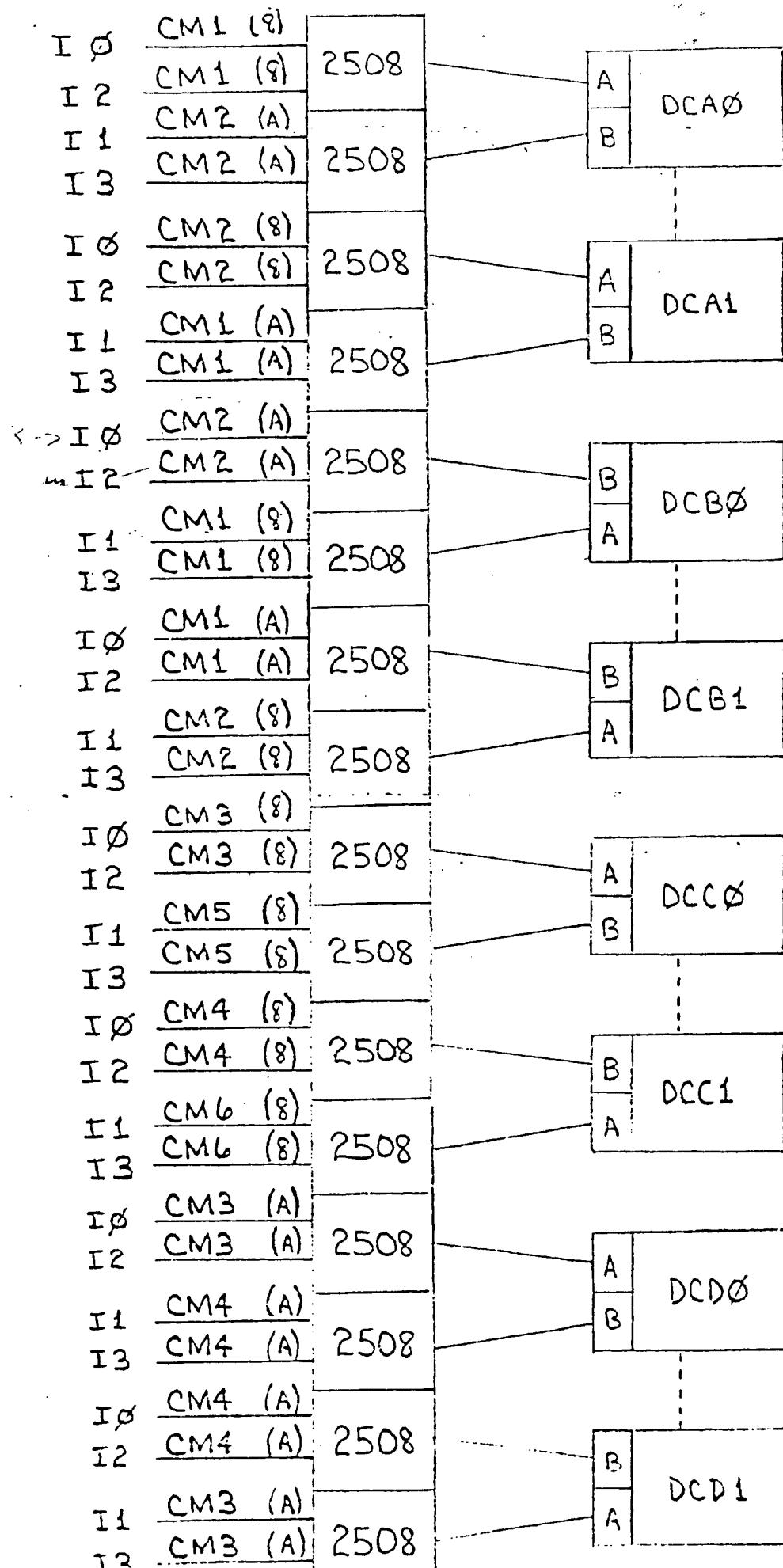
1
OFF LINE
APPL 0
APPL 1

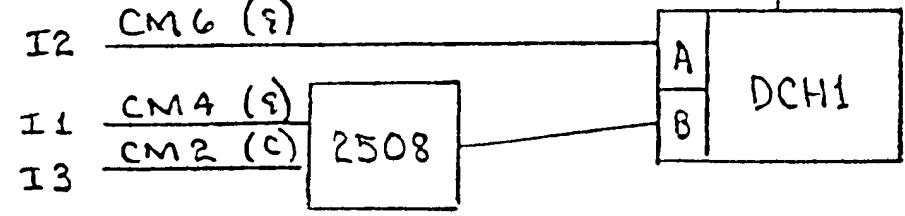
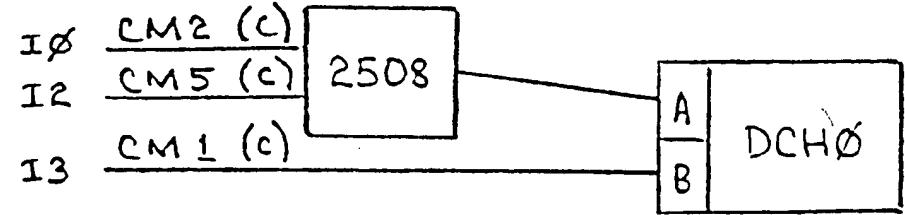
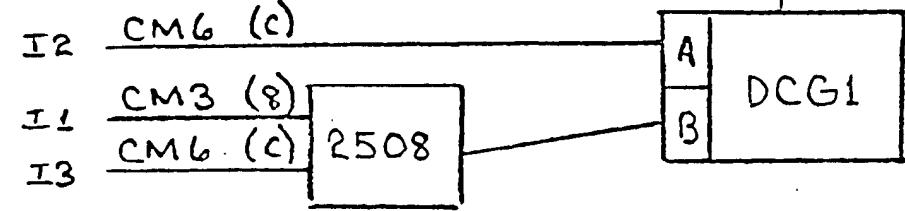
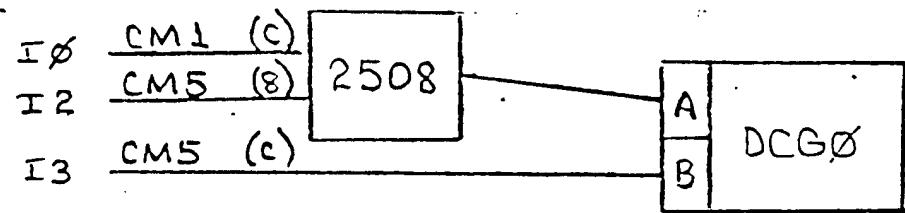
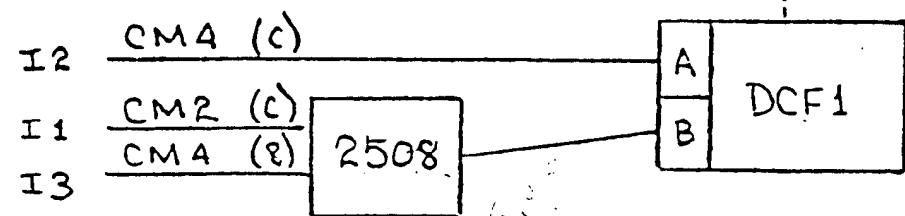
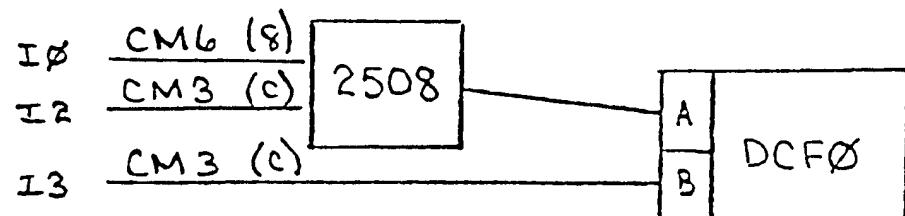
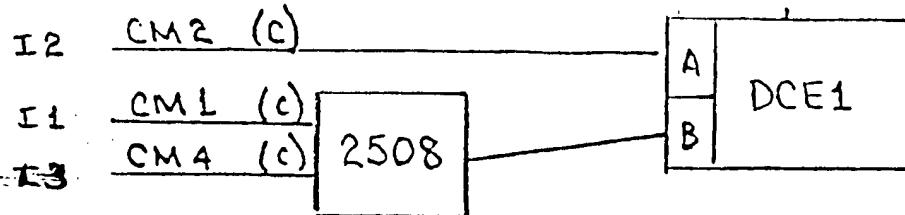
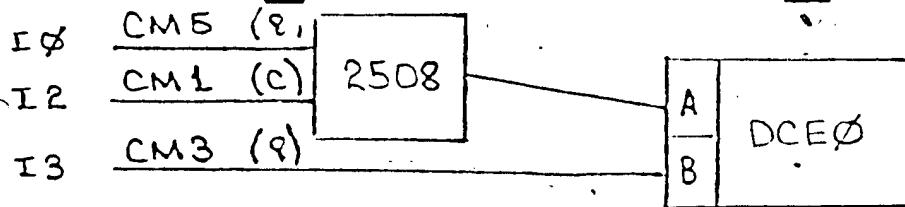
IOU

INTERFACE EN
IOU 2

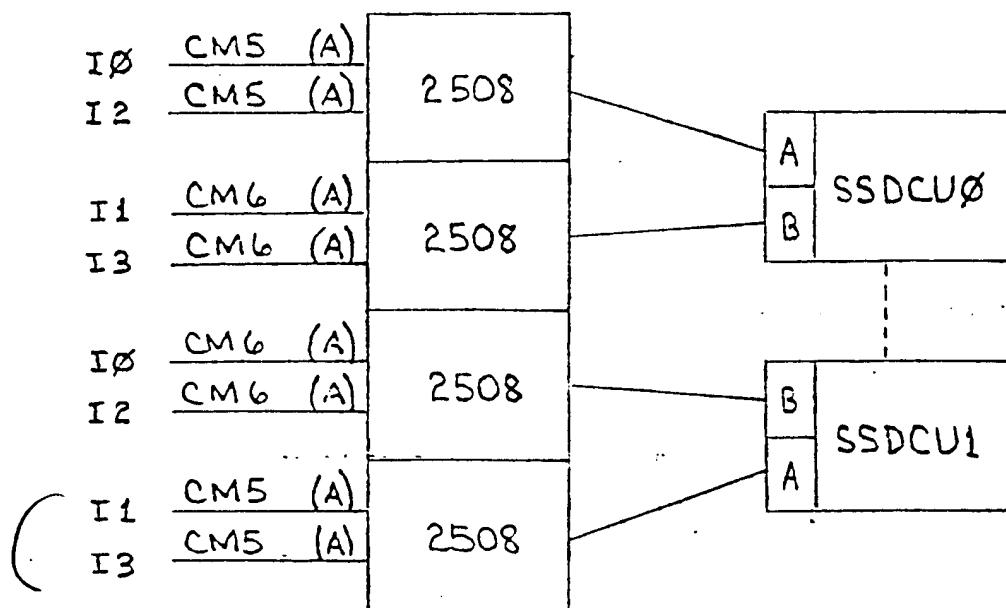
2
OFF LINE
APPL 0
APPL 1

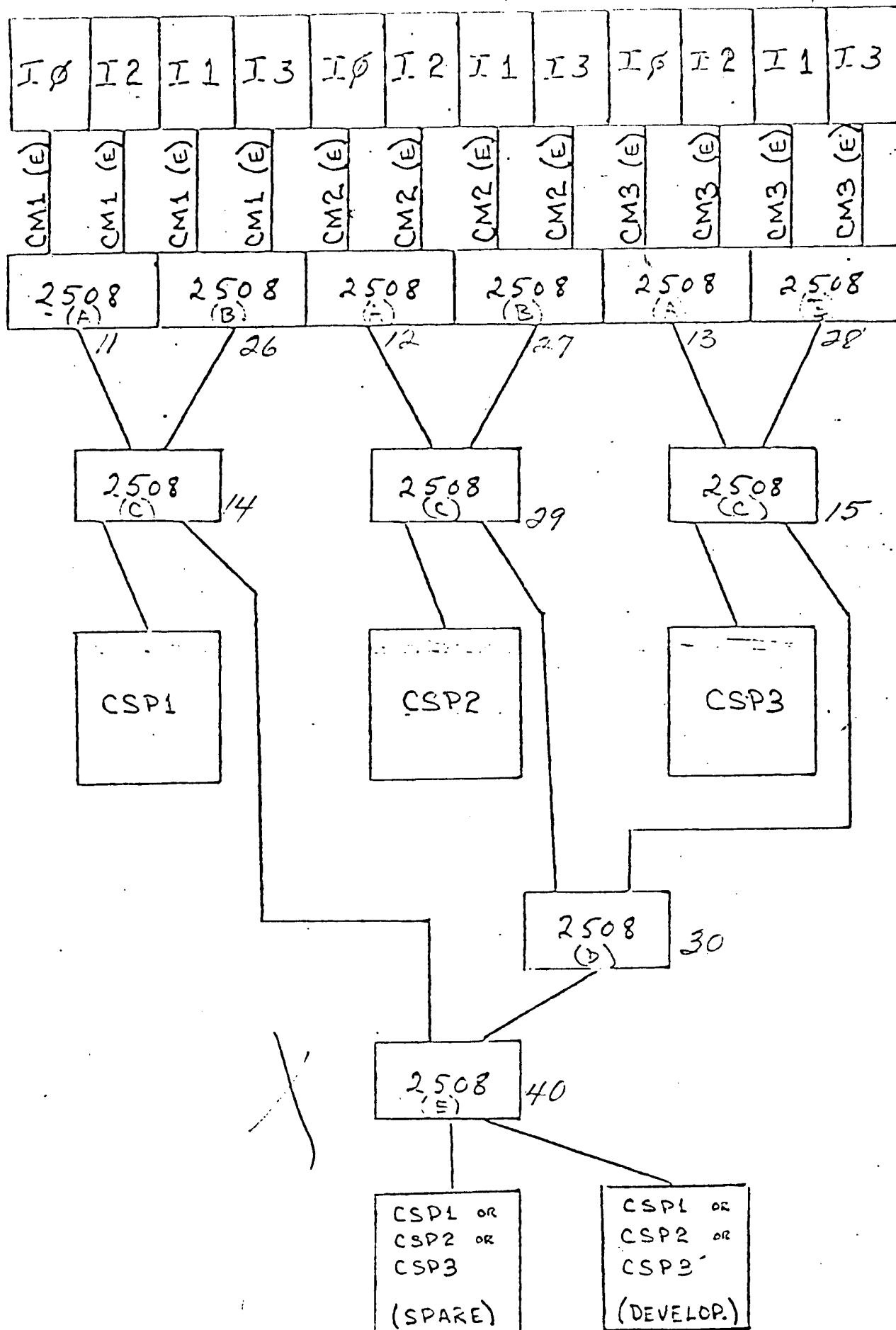
3
OFF LINE
APPL 0
APPL 1



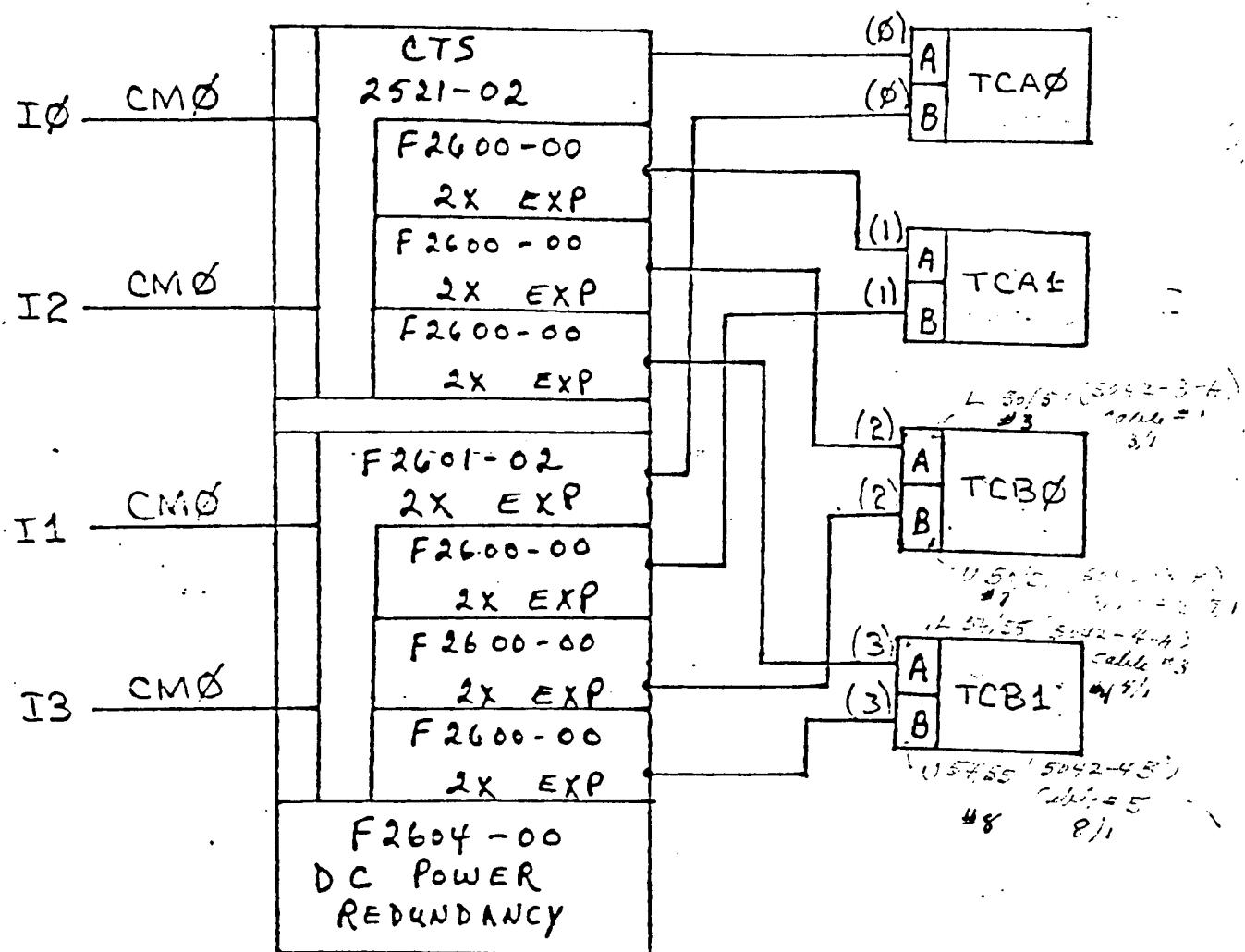


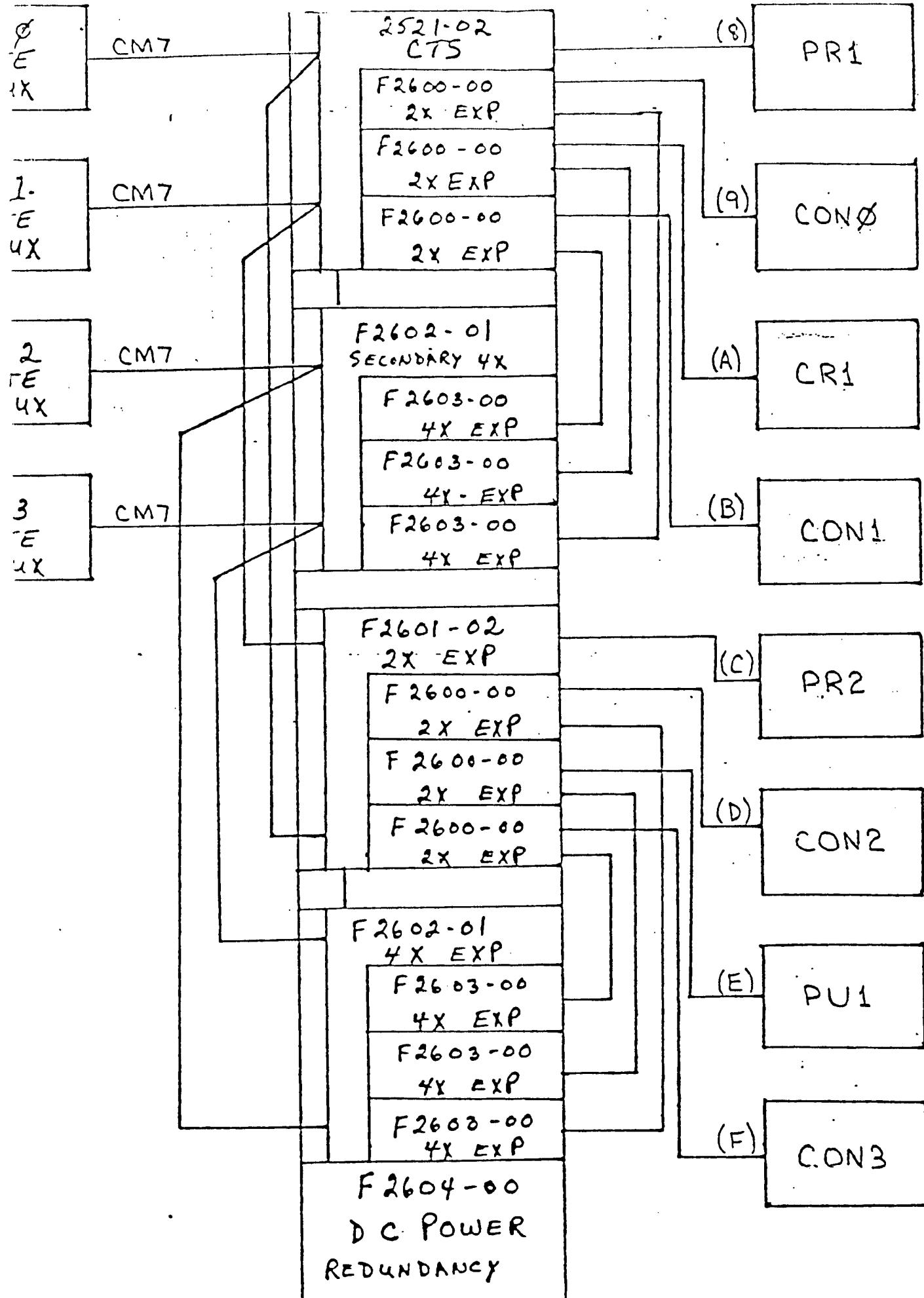
SOLID STATE DRUMS

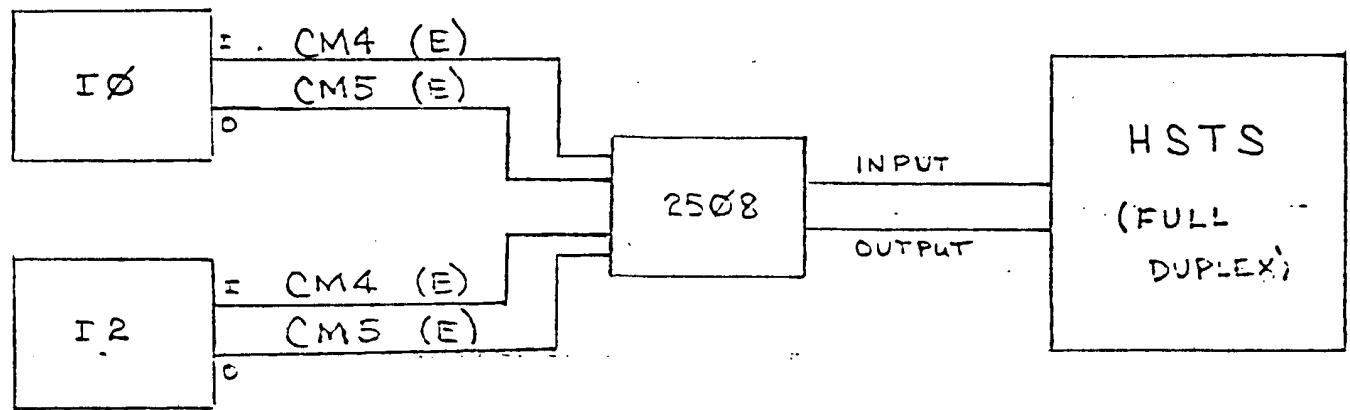




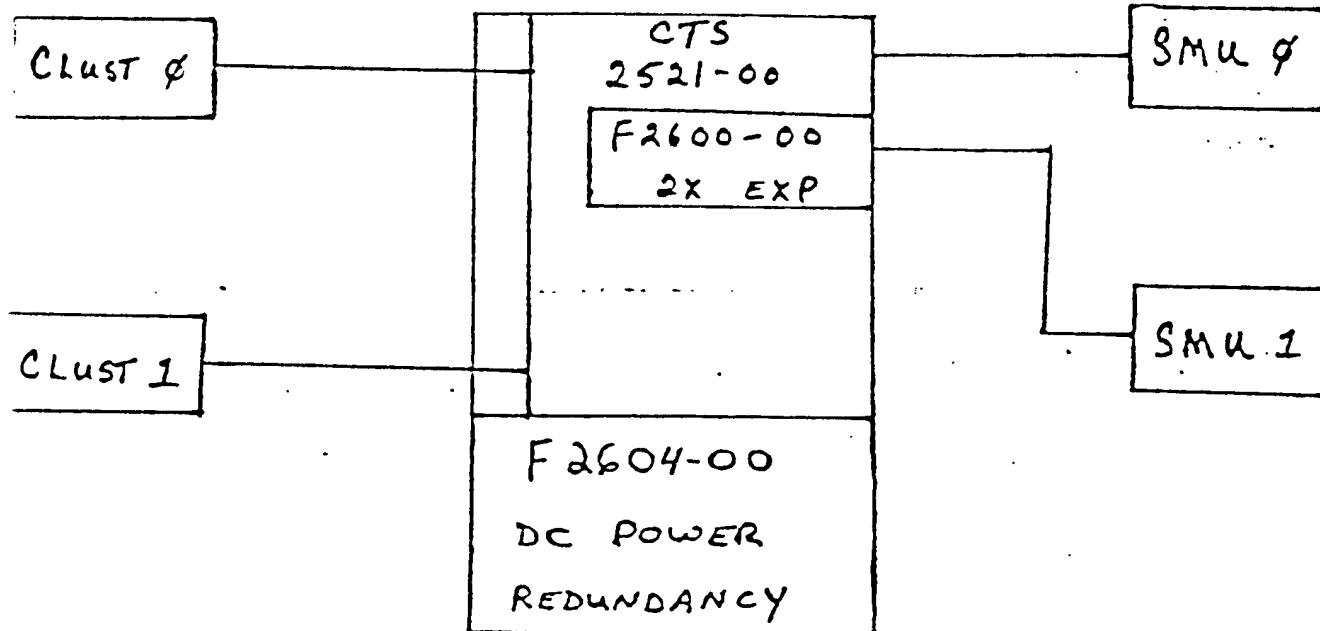
TAPE CONTROL UNITS







SYSTEM MAINTENANCE UNITS



September 1, 1981

MEMORANDUM FOR: CHIEF, SPS

FROM

SUBJECT : 1100/84 Configuration

STAT

The attached document is the revised 1100/84 configuration. The following changes have been made since the previous release of this document (Aug 20, 1981).

1. The control unit names are changed to match the names used in the "SYSGEN".
2. The position of the current CACHE-DISK subsystem was changed. It is now named "DCA0" and "DCA1"
3. Two new diagrams have been added:
 - a. MULTI-ACCESS SUBSYSTEMS - This indicates the positioning of "word-channel-module" devices on the SAU
 - b. BCTS SUBSYSTEMS - This diagram shows the positioning of "BYTE-MUX" and "BLOCK-MUX" devices on the SAU

STAT

Distribution:

- 1 - Addressee
- 1 - COTR
- 1 - CH/PB
- 1 - CH/OSS
- 1 -
- 1 -
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- UNIVAC C.E.
- Originator

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